

## EAST Search History

## EAST Search History (I nterference)

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L5	10	((virtual emulat\$4 simulat\$5 test\$3) adj (microcontroller) and (lock\$step\$3 synchroniz\$7)).clm.	US-PGPUB; USPAT; UPAD	OR	OFF	2010/05/28 15:25
L6	56	((virtual emulat\$4 simulat\$5 test\$3) adj (processor) and (lock\$step\$3 synchroniz\$7)).clm.	US-PGPUB; USPAT; UPAD	OR	OFF	2010/05/28 15:28
L7	5	((virtual emulat\$4 simulat\$5 test\$3) adj (cpu) and (lock\$step\$3 synchroniz\$7)).clm.	US-PGPUB; USPAT; UPAD	OR	OFF	2010/05/28 15:32
L8	16	((virtual emulat\$4 simulat\$5 test\$3) adj (microprocessor) and (lock\$step\$3 synchroniz\$7)).clm.	US-PGPUB; USPAT; UPAD	OR	OFF	2010/05/28 15:33
L9	2082	((synchron\$7 lock\$step\$5) and (halt\$4 break\$4 stop\$4) and (cpu processor microprocessor microcontroller)).clm.	US-PGPUB; USPAT; UPAD	OR	OFF	2010/05/28 15:50
L10	606	((synchron\$7 lock\$step\$5) same (halt\$4 break\$4 stop\$4) and (cpu processor microprocessor microcontroller)).clm.	US-PGPUB; USPAT; UPAD	OR	OFF	2010/05/28 15:50
L11	297	((synchron\$7 lock\$step\$5) same (halt\$4 break\$4 stop\$4) same (cpu processor microprocessor microcontroller)).clm.	US-PGPUB; USPAT; UPAD	OR	OFF	2010/05/28 15:50

L12	381	((synchron\$7 lock\$step\$5) and (halt\$4 break\$4 stop\$4) and (cpu processor microprocessor microcontroller) and (boot\$4 initial\$7)).clm.	US-PGPUB; USPAT; UPAD	OR	OFF	2010/05/28 15:51
L13	14	((synchron\$7 lock\$step\$5) same (halt\$4 break\$4 stop\$4) same (cpu processor microprocessor microcontroller) same (boot\$4 initial\$7)).clm.	US-PGPUB; USPAT; UPAD	OR	OFF	2010/05/28 15:52

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